

PARENTS: _____

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O.I.P.E.		PATENT DATE
SCANNED	Q.A.	

ANTS Frankie Roohpavvar

ME Zero latency-zero bus turnaround synchronous flash memory

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE FEE	
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